

**METHOD AND APPARATUS FOR ALIAS SUPPRESSED DIGITIZING OF
HIGH FREQUENCY ANALOG SIGNALS**

*This application is a continuation of a 371 of
PCT/IB03/00110 filed January 17, 2003.*

BACKGROUND OF THE INVENTION

1. Field of the Invention

The present invention relates generally to the field of analog signal digitizing. More specifically the present invention is addressed to a problem of alias suppressed signal sampling at a frequency considerably lower than the upper frequency of the said analog signal. A method and apparatus covered by the invention are adapted to digitizing the high frequency signals in applications related to versatile digital signal processing (e.g. oscilloscope).

2. Description of Prior Art